

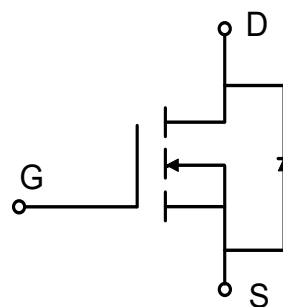
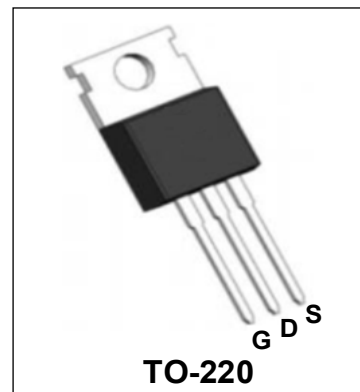
70V N-Channel Enhancement Mode Power MOSFET

Description

WMK100N07TS uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Features

- $V_{DS} = 70V$, $I_D = 100A$
 $R_{DS(on)} < 7.4m\Omega$ @ $V_{GS} = 10V$
- Green Device Available
- Low Gate Charge
- Advanced High Cell Density Trench Technology
- 100% EAS Guaranteed



Applications

- Synchronous Rectification
- DC/DC Converter
- Moto Control

Absolute Maximum Ratings (Tc = 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	70	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	Tc=25°C	I_D	100	A
	Tc=100°C		63	
Pulsed Drain Current ⁴		I_{DM}	400	A
Single Pulse Avalanche Energy ³		EAS	217.8	mJ
Total Power Dissipation ⁴	Tc=25°C	P_D	133	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	$R_{\theta JA}$	42	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	0.94	°C/W

Electrical Characteristics (Tc = 25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	70	-	-	V
Gate-body Leakage current		I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	I _{DSS}	V _{DS} =70V, V _{GS} = 0V	-	-	1	μA
	T _J =55°C			-	-	100	
Gate-Threshold Voltage		V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2	3	4	V
Drain-Source on-Resistance ²		R _{DS(on)}	V _{GS} = 10V, I _D = 20A	-	5.7	7.4	mΩ
Forward Transconductance ²		g _{fs}	V _{DS} =5V , I _D =20A	-	76	-	S
Dynamic Characteristics							
Input Capacitance		C _{iss}	V _{DS} = 35V, V _{GS} =0V, f =1MHz	-	4030	-	pF
Output Capacitance		C _{oss}		-	271	-	
Reverse Transfer Capacitance		C _{rss}		-	189	-	
Switching Characteristics							
Gate Resistance		R _g	V _{DS} = 0V, V _{GS} =0V, f =1MHz	-	1.2	-	Ω
Total Gate Charge(10V)		Q _g	V _{GS} = 10V, V _{DS} = 35V, I _D =20A	-	92	-	nC
Gate-Source Charge		Q _{gs}		-	22	-	
Gate-Drain Charge		Q _{gd}		-	31	-	
Turn-On Delay Time		t _{d(on)}	V _{GS} =10V, V _{DD} =35V, R _G = 4.7Ω, I _D = 20A	-	36	-	ns
Rise Time		t _r		-	77	-	
Turn-Off Delay Time		t _{d(off)}		-	92	-	
Fall Time		t _f		-	34	-	
Drain-Source Body Diode Characteristics							
Diode Forward Voltage ²		V _{SD}	I _S = 1A, V _{GS} = 0V	-	-	1	V
Continuous Source Current ^{1,5}		I _S	V _G =V _D =0V, Force Current	-	-	100	A

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is $V_{DD} = 40V, V_{GS} = 10V, L = 0.4mH, I_{AS} = 33A$
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

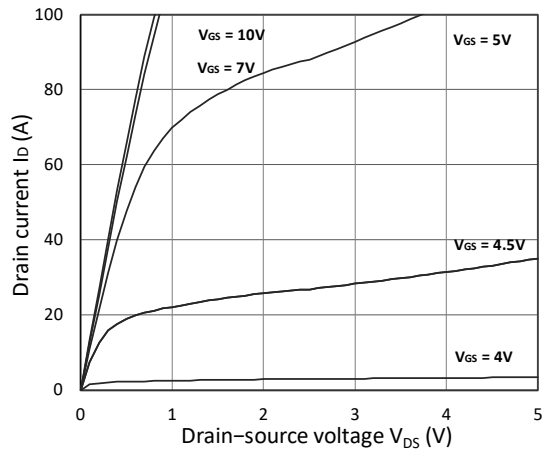


Figure 1. Output Characteristics

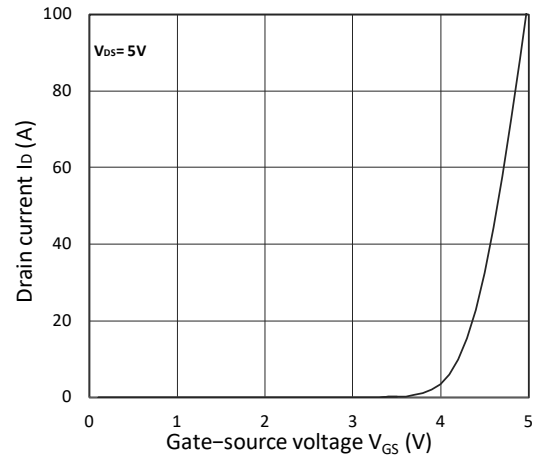


Figure 2. Transfer Characteristics

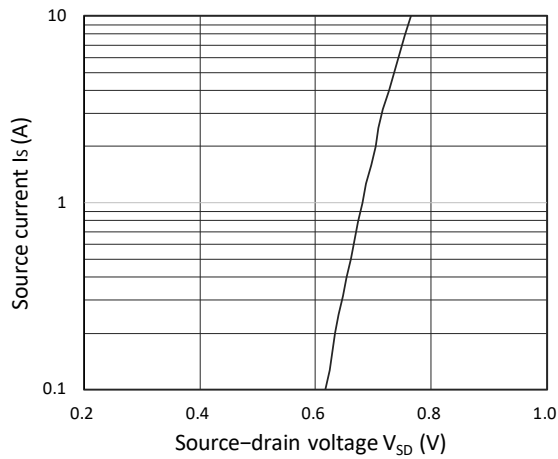
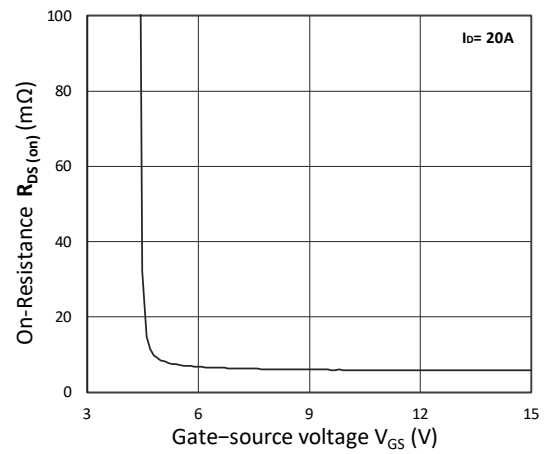
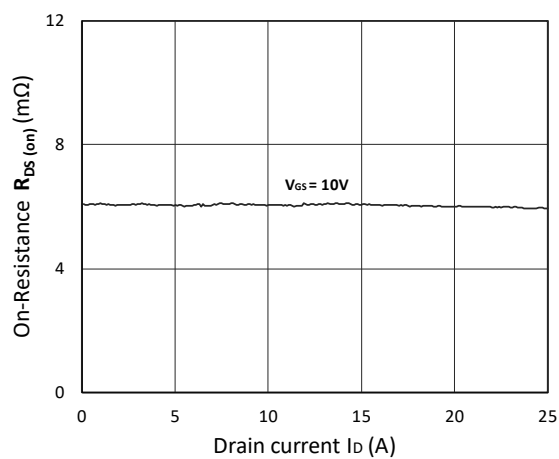
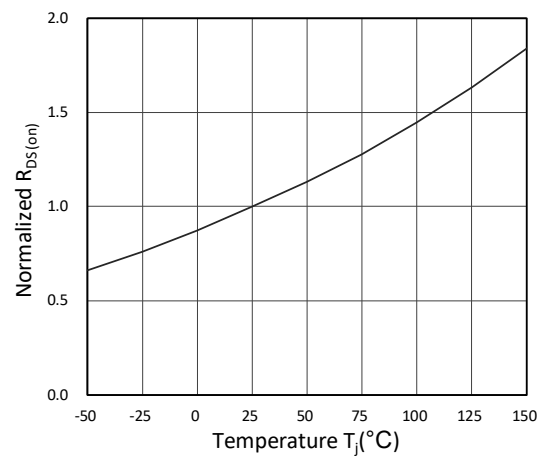


Figure 3. Forward Characteristics of Reverse

Figure 4. $R_{DS(on)}$ vs. V_{GS} Figure 5. $R_{DS(on)}$ vs. I_D Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

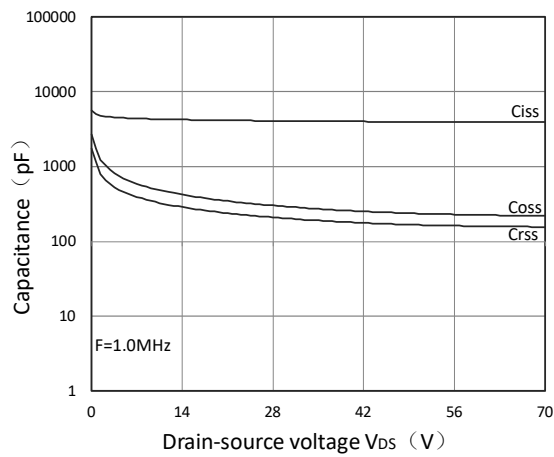


Figure 7. Capacitance Characteristics

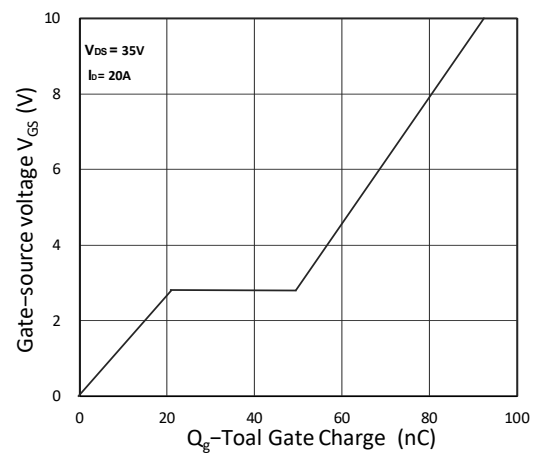


Figure 8. Gate Charge Characteristics

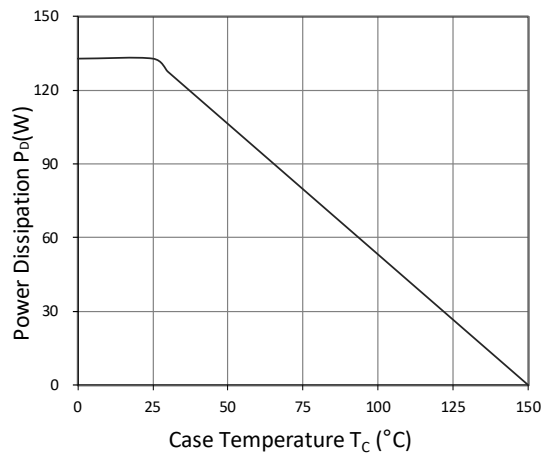


Figure 9. Power Dissipation

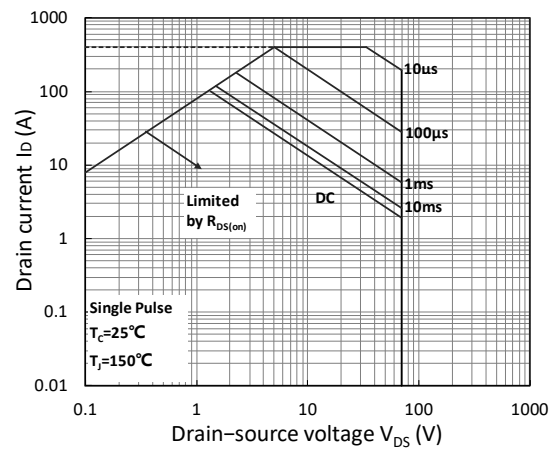


Figure 10. Safe Operating Area

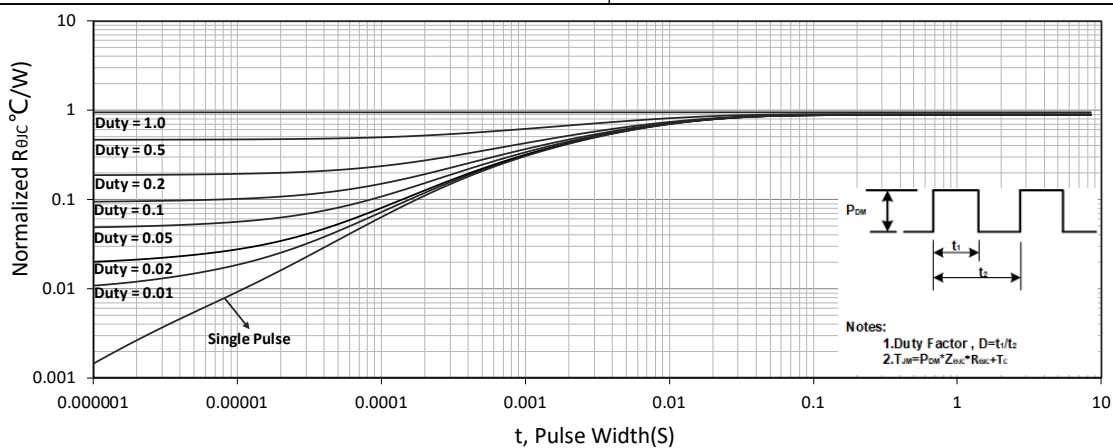


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

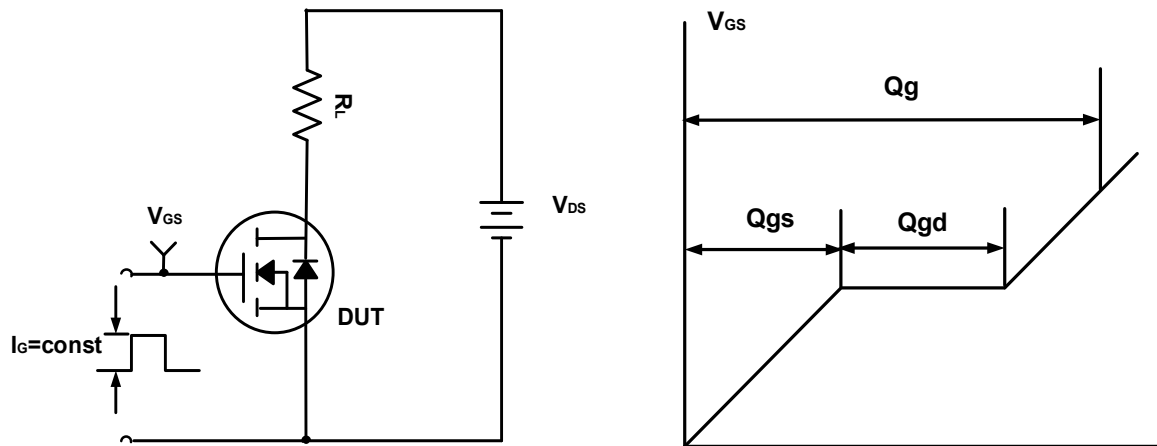


Figure A. Gate Charge Test Circuit & Waveforms

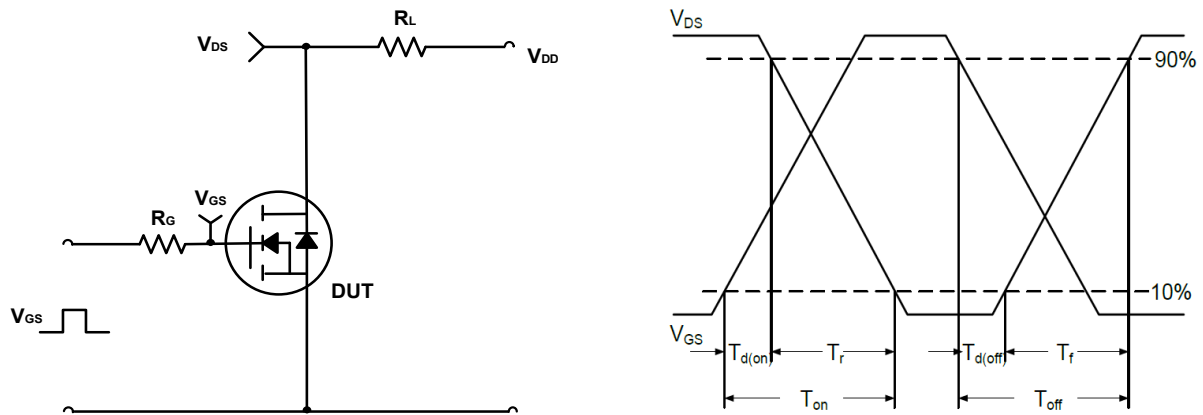


Figure B. Switching Test Circuit & Waveforms

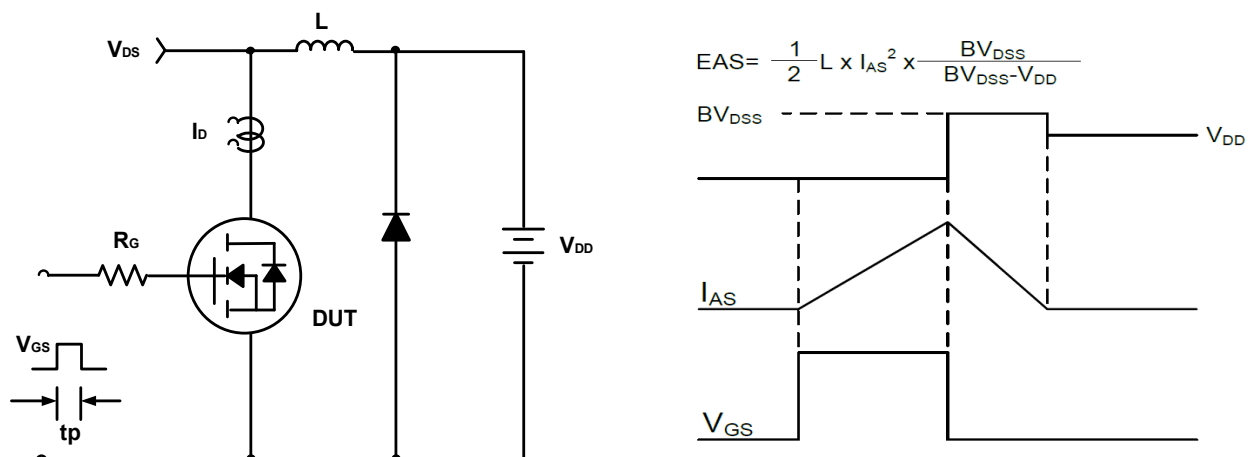
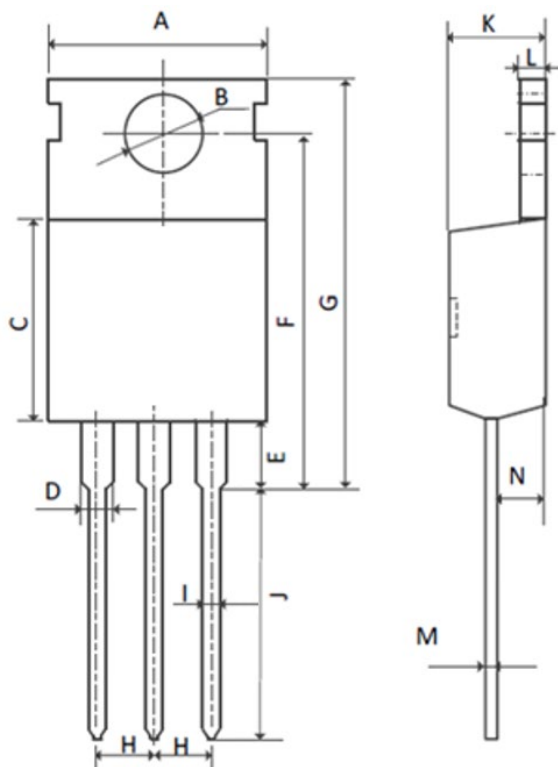


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for TO-220

COMMON DIMENSIONS

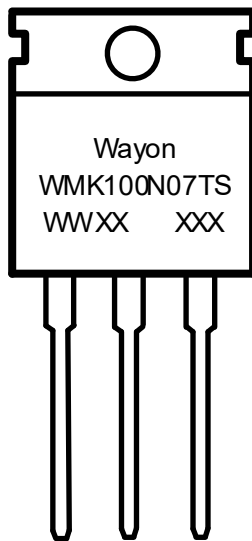


SYMBOL	MM	
	MIN	MAX
A	9.70	10.30
B	3.40	3.80
C	8.80	9.40
D	1.17	1.47
E	2.60	3.50
F	15.10	16.70
G	19.55MAX	
H	2.54REF	
I	0.70	0.95
J	9.35	11.00
K	4.30	4.77
L	1.20	1.45
M	0.40	0.65
N	2.20	2.60

Ordering Information

Part	Package	Marking	Packing method
WMK100N07TS	TO-220	WMK100N07TS	Tube

Marking Information



WMK100N07TS = Device code

WWXX XXX= Date code

Contact Information

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